

# VERIGY 93000

## STATS ChipPAC Milpitas



The Verigy 93000 SOC Series Test System combines digital and analog test capabilities for testing the most demanding mixed-signal System-on-a-Chip (SOC) with data rates up to 1 Gb/s.

STATS ChipPAC offers 5 standard configurations for the Verigy 93000 SOC Series platform. For for the most current configurations and tester availability, please contact STATS ChipPAC Test Services.

Configurations	1	2	3	4	5
<b>Digital Options</b>					
I/O Pins	512/1024	528/1024	656/1024	384/DutScale	640/1024
Frequency	P600	P600	C400E	PS400	PS800
Vector Memory	28M	28M	28M	47M	54M
Scan	112M (16ch)	112M (32ch)	-	-	-
<b>Analog Options*</b>					
Time Interval Analyzer	1	1	-	ppTIA	ppTIA
HS Digitizer (WDA)	1	1	1	-	-
HR Digitizer (WDB)	1	1	-	-	-
HS AWG (WGB)	1	1	-	-	-
UHS AWG (WGC)	1	-	-	-	-
BHS AWG (WGD)	-	-	-	-	-
HR AWG (WGA)	1	1	1	-	-
DHS Sampler (SPA)	1	-	-	-	-
MAV8	-	-	1	-	-
BAA	-	-	-	-	1
MCB	-	-	-	1	1
<b>Alt. Master Clock</b>					
AMC (250KHz-3GHz)	2	1	-	-	-
AMC (NP2500)	-	2	-	-	-
<b>DC Options</b>					
GPDPS	4	4	3	-	1
HCPS	-	1	-	-	-
MSDPS	-	-	-	2	5
DPS32	-	-	-	1	-
<b>RF Options</b>					
RFF	-	-	-	2	-
RFE	-	-	-	2	-

\* HS=High Speed HR=High Resolution UHS=Ultra High Speed  
DHS=Dual High Speed BHS=Broadband High Speed

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